



CWD 2/83

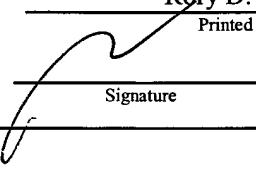
## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 09/802,108

Filed: March 8, 2001

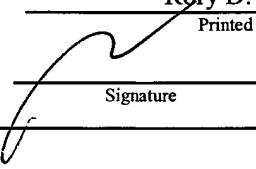
Inventor(s): Ashley Saulsbury  
Michael Parkin  
Daniel S. RiceTitle: COMPUTER  
PROCESSING  
ARCHITECTURE  
HAVING A SCALABLE  
NUMBER OF  
PROCESSING PATHS  
AND PIPELINES§ Examiner: William M. Treat  
§ Group/Art Unit: 2183  
§ Atty. Dkt. No: 6000-00800

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

  
Rory D. Rankin

Printed Name

September 21, 2005

  
Signature

Date

**RESPONSE TO OFFICE ACTION OF**  
**JUNE 30, 2005**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of June 30, 2005, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.